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"Shadow register" + access + privilege

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[PowerPC Compiler Writer's Guide - Glossary](#)

privilege level One of two access-permission levels: supervisor (PR=0) or ... **shadow register** A register that can be updated by instructions that are ...

wall.riscom.net/books/proc/ppc/cwg/glossary.html - 83k - [Cached](#) - [Similar pages](#)

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In addition they contain a variety of flags and **privilege** information. ... descriptor into a **shadow register**. **Access** can now proceed at CPU register speeds. ...

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Least **privilege**: don't grant more rights than required ... subject's **access** rights after the first **access**. - **shadow register** is the place used to store the ...

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[PA-RISC 1.1 Instruction Set Architecture: Appendix A - Glossary](#)

Privilege Level: The PA-RISC **access** control mechanisms are based on 4 **privilege** ... **Shadow Register** (SHR): A register into which the contents of a general ...

sequoia.ict.pwr.wroc.pl/~iro/RISC/sm/www.hp.com/appendices-1.html - 36k - [Cached](#) - [Similar pages](#)

[Descriptor-Based Protection Systems](#)

Whenever a pointer register containing the unique identifier of an **access** controller is first used, the **shadow register** is loaded with a capability ...

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extension, an effective **shadow-register** binding algorithm is ... controlled by the custom logic with read-only **privilege**. It is ...

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page **privilege** levels: program execution based on code residing at a less privileged level cannot **access** data. segments or jump to code segments that are at ...

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Security and protection - invasive software, unauthorized **access**. Keywords ... the victim system, an attacker has to gain sufficient **privilege**. ...

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shadow register need to be encoded as additional control bits in the instruction format. ... controlled by the custom logic with read-only **privilege**. It is ...

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13th USENIX Security Symposium — Technical Paper

The principles that underly our motivation are: first, minimize available **privilege** (ie sensitive data **access**) throughout the lifetime of a program; second, ...

www.usenix.org/events/sec04/tech/chow/chow_html/index.html - 104k -

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